

esp8266/esp8266-wiki

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<html> <div class=„markdown-body“>

<p>ESP8266EX chip pinout is something like this:</p>

<p><img src=„<https://github.com/esp8266/esp8266-wiki/raw/master/images/esp8266ex-layout.jpg>“ alt=„ESP8266EX Pin Layout Diagram“/></p> <table><thead><tr><th align=„center“>Pin</th><th>Name</th> <th align=„center“>Type</th> <th>GPIO</th> <th>Function</th><th>Conditions for use</th> </tr></thead><tbody><tr><td align=„center“>1</td><td>VDDA</td> <td align=„center“>P</td> <td/> <td>Analog Power 3.0 ~3.6V</td> <td/></tr><tr><td align=„center“>2</td> <td>LNA</td> <td align=„center“>I/O</td> <td/> <td>RF Antenna Interface,Chip Output Impedance=50 Ω; No matching required but we recommend that the 1/4λ type matching network is retained.</td> <td/> </tr><tr><td align=„center“>3</td><td>VDD3P3</td> <td align=„center“>P</td> <td/> <td>Amplifier Power 3.0~3.6V</td> <td/></tr><tr><td align=„center“>4</td> <td>VDD3P3</td> <td align=„center“>P</td> <td/> <td>Amplifier Power 3.0~3.6V</td> <td/></tr><tr><td align=„center“>5</td><td>VDD_RTC</td> <td align=„center“>P</td> <td/> <td>NC(1.1V)</td> <td/> </tr><tr><td align=„center“>6</td> <td>TOUT</td> <td align=„center“>I</td> <td/> <td>ADC Pin</td> <td/></tr><tr><td align=„center“>7</td> <td>CHIP_EN</td> <td align=„center“>I</td> <td/> <td>Chip Enable. High: On, chip works properly; Low: Off, small current</td> <td/></tr><tr><td align=„center“>8</td> <td>XPD_DCDC</td> <td align=„center“>I/O</td> <td>GPIO16</td> <td>Deep-Sleep Wakeup</td> <td/> </tr><tr><td align=„center“>9</td> <td>MTMS</td> <td align=„center“>I/O</td> <td>GPIO14</td> <td>HSPICLK</td> <td/> </tr><tr><td align=„center“>10</td> <td>MTDI</td> <td align=„center“>I/O</td> <td>GPIO12</td> <td>HSPIQ</td> <td/> </tr><tr><td align=„center“>11</td> <td>VDDPST</td> <td align=„center“>P</td> <td/> <td>Digital/IO Power Supply (1.8V~3.3V)</td> <td/> </tr><tr><td align=„center“>12</td> <td>MTCK</td> <td align=„center“>I/O</td> <td>GPIO13</td> <td>HSPID</td> <td/> </tr><tr><td align=„center“>13</td> <td>MTDO</td> <td align=„center“>I/O</td> <td>GPIO15</td> <td>HSPICS</td> <td/> </tr><tr><td align=„center“>14</td> <td>GPIO2</td> <td align=„center“>I/O</td> <td>GPIO2</td> <td>UART Tx during flash programming</td> <td/> </tr><tr><td align=„center“>15</td> <td>GPIO0</td> <td align=„center“>I/O</td> <td>GPIO0</td> <td>SPICS2</td> <td/></tr><tr><td align=„center“>16</td> <td>GPIO4</td> <td align=„center“>I/O</td> <td>GPIO4</td> <td/> <td/> </tr><tr><td align=„center“>17</td> <td>VDDPST</td> <td align=„center“>P</td> <td/> <td>Digital/IO Power Supply (1.8V~3.3V)</td> <td/> </tr><tr><td align=„center“>18</td> <td>SDIO_DATA_2</td> <td align=„center“>I/O</td> <td/> <td>Connect to SD_D2 (Series R 200Ω; SPIHD; HSPIHD</td> <td/> </tr><tr><td align=„center“>19</td> <td>SDIO_DATA_3</td> <td align=„center“>I/O</td> <td/> <td>Connect to SD_D3 (Series R 200Ω; SPIWP; HSPIWP</td> <td/> </tr><tr><td align=„center“>20</td> <td>SDIO_CMD</td> <td align=„center“>I/O</td> <td/> <td>Connect to SD_CMD(Series R 200Ω; SPICS0</td> <td/> </tr><tr><td align=„center“>21</td> <td>SDIO_CLK</td> <td align=„center“>I/O</td> <td/> <td>Connect to SD_CLK (Series R 200Ω; SPICLK</td> <td/> </tr><tr><td align=„center“>22</td> <td>SDIO_DATA_0</td> <td align=„center“>I/O</td> <td/> <td>Connect to SD_D0 (Series R 200Ω; SPIQ</td> <td/> </tr><tr><td align=„center“>23</td> <td>SDIO_DATA_1</td> <td align=„center“>I/O</td> <td/> <td>Connect

to SD_D1 (Series R 200Ω); SPID</td> <td/> </tr><tr><td align=„center“>24</td>
<td>GPIO5</td> <td align=„center“>I/O</td> <td>GPIO5</td> <td/> <td/> </tr><tr><td
align=„center“>25</td> <td>U0RXD</td> <td align=„center“>I/O</td> <td>GPIO3</td>
<td>UART Rx during flash programming</td> <td/> </tr><tr><td align=„center“>26</td>
<td>U0TXD</td> <td align=„center“>I/O</td> <td>GPIO1</td> <td>UART Tx during flash
programming; SPICS1</td> <td/> </tr><tr><td align=„center“>27</td> <td>XTAL_OUT</td> <td
align=„center“>I/O</td> <td/> <td>Connect to crystal output, can be used to provide BT clock
input</td> <td/> </tr><tr><td align=„center“>28</td> <td>XTAL_IN</td> <td
align=„center“>I/O</td> <td/> <td>Connect to crystal input</td> <td/> </tr><tr><td
align=„center“>29</td> <td>VDDD</td> <td align=„center“>P</td> <td/> <td>Analog Power
3.0~3.6V</td> <td/> </tr><tr><td align=„center“>30</td> <td>VDDA</td> <td
align=„center“>P</td> <td/> <td>Analog Power 3.0~3.6V</td> <td/> </tr><tr><td
align=„center“>31</td> <td>RES12K</td> <td align=„center“>I</td> <td/> <td>Connect to
series R 12kΩ to ground</td> <td/> </tr><tr><td align=„center“>32</td>
<td>EXT_RSTB</td> <td align=„center“>I</td> <td/> <td>External reset signal (Low:
Active)</td> <td/> </tr><tr><td align=„center“>Note: GPIO2, GPIO0, MTDO can be configurable as
3-bit SDIO mode </td> <td/> <td align=„center“/> <td/> <td/> <td/>
</tr></tbody></table></div> </html>

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